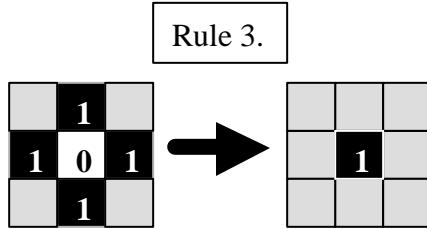
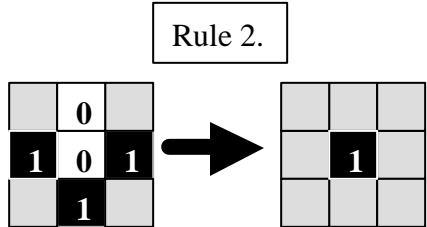
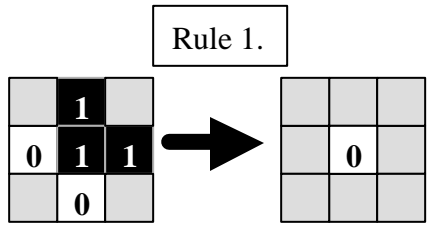


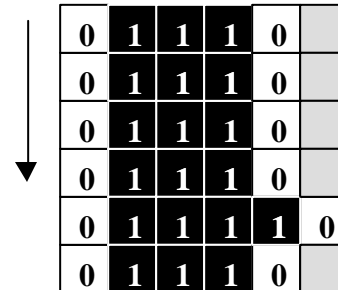


The logic function: B AND NOT A.  
 Case III, B input only. 15-cycle animation.

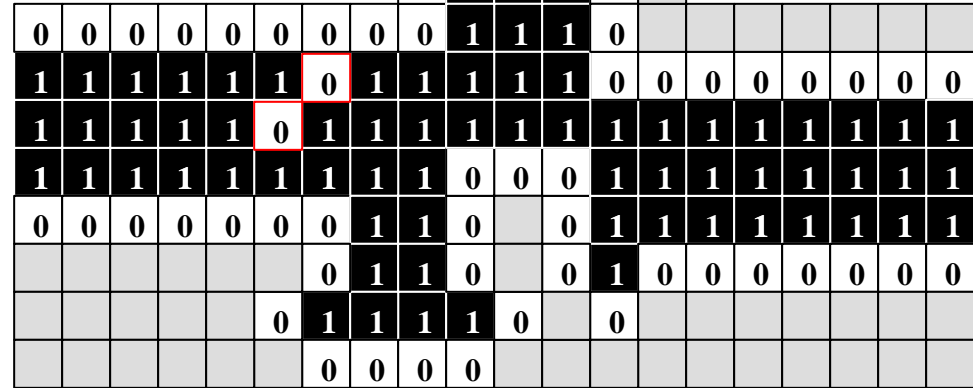


Cycle 1

A

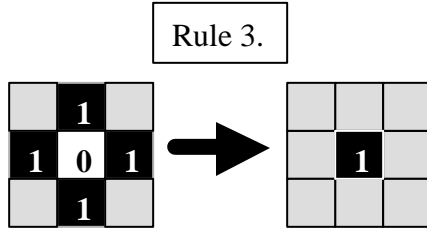
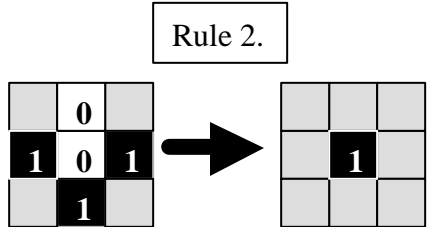
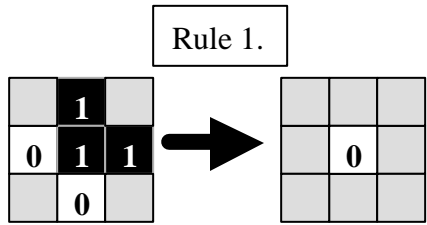


B



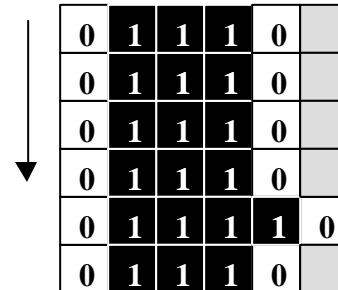
output wire

The logic function: B AND NOT A.  
 Case III, B input only. 15-cycle animation.

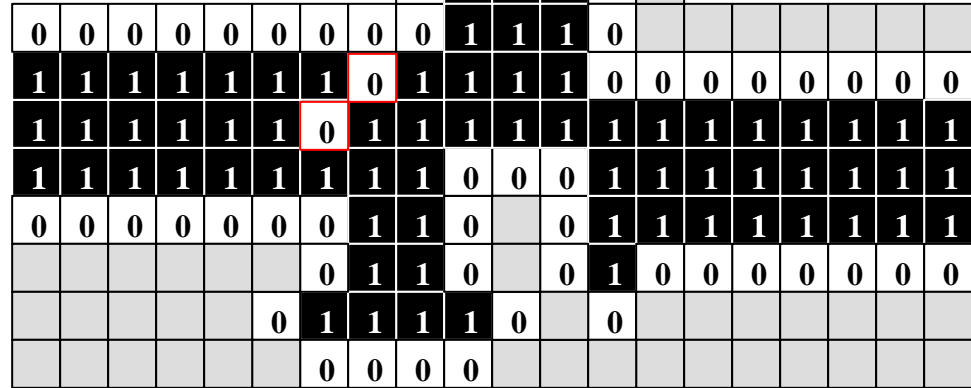


Cycle 2

A



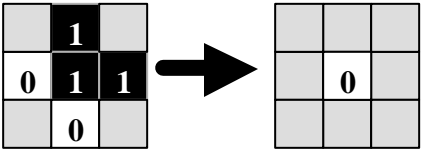
B



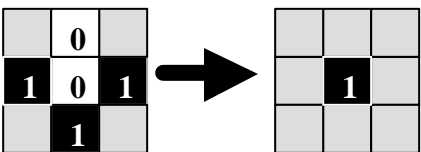
output wire

The logic function: B AND NOT A.  
 Case III, B input only. 15-cycle animation.

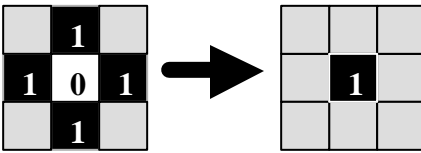
Rule 1.



Rule 2.

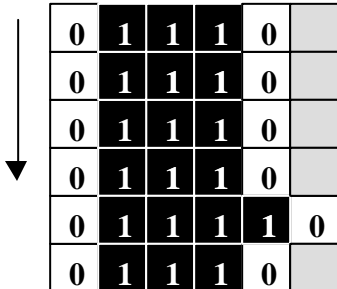


Rule 3.

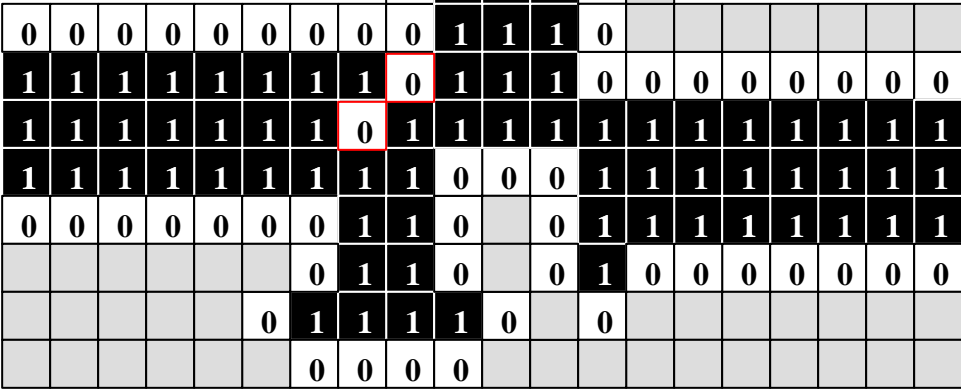


Cycle 3

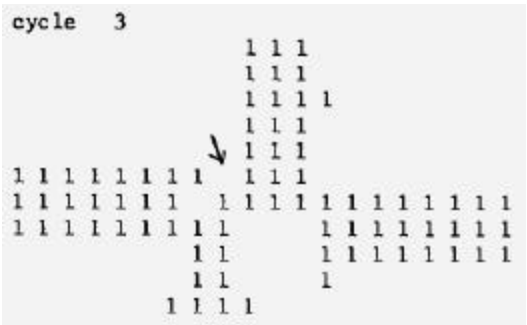
A



B

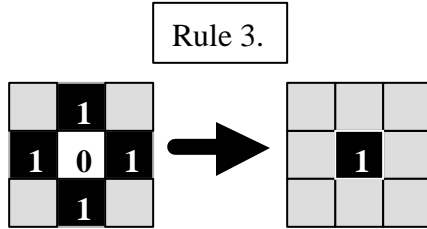
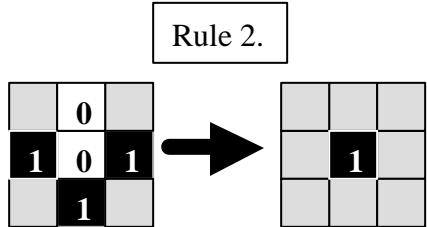
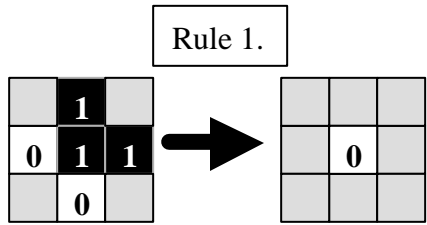


output wire



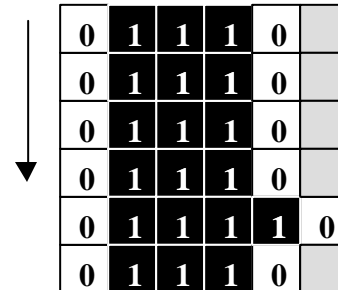


The logic function: B AND NOT A.  
 Case III, B input only. 15-cycle animation.

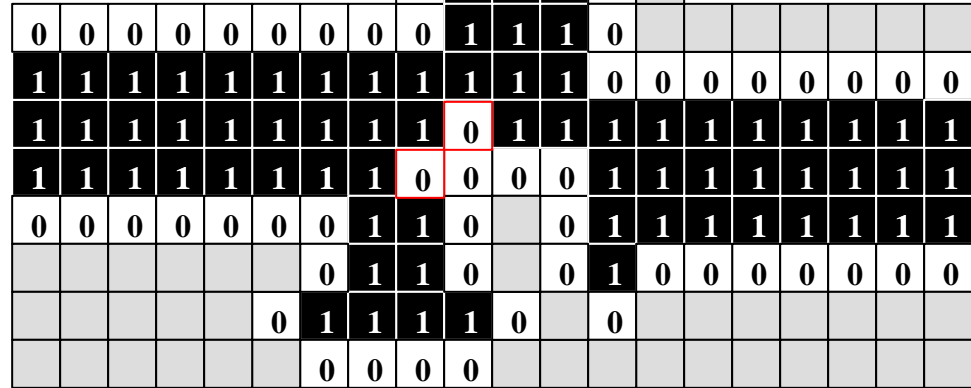


Cycle 5

A

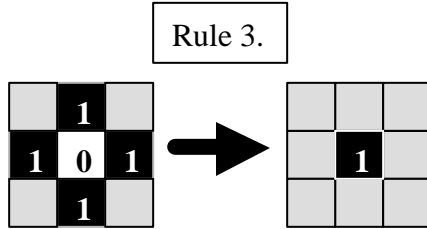
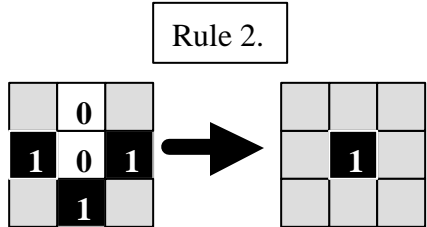
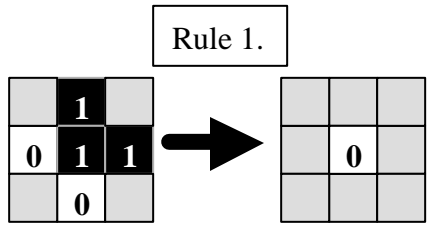


B



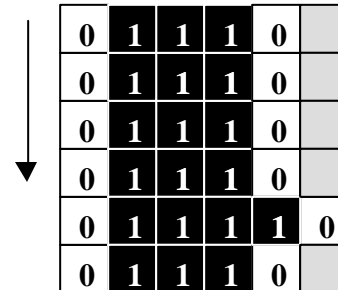
output wire

The logic function: B AND NOT A.  
 Case III, B input only. 15-cycle animation.

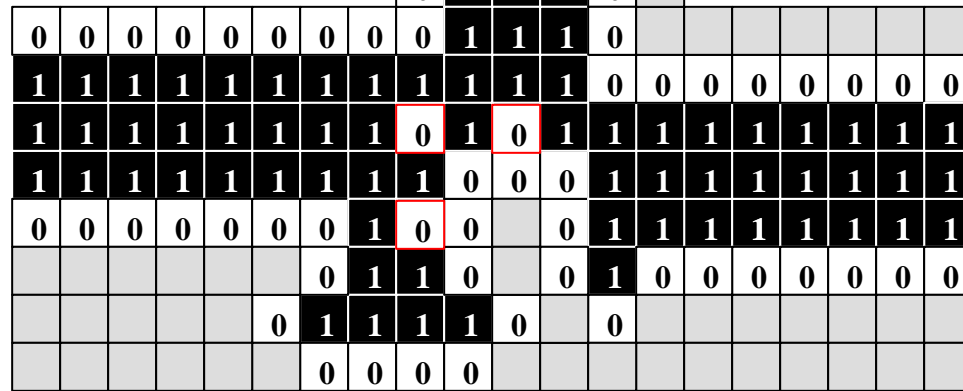


Cycle 6

A



B



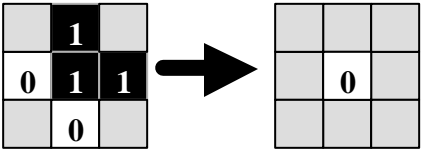
output wire



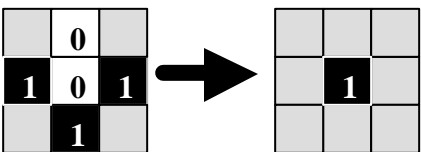


The logic function: B AND NOT A.  
 Case III, B input only. 15-cycle animation.

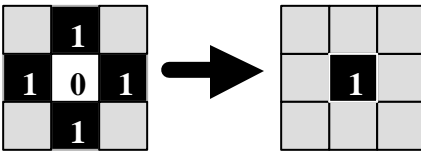
Rule 1.



Rule 2.

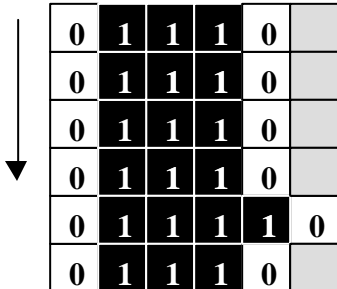


Rule 3.

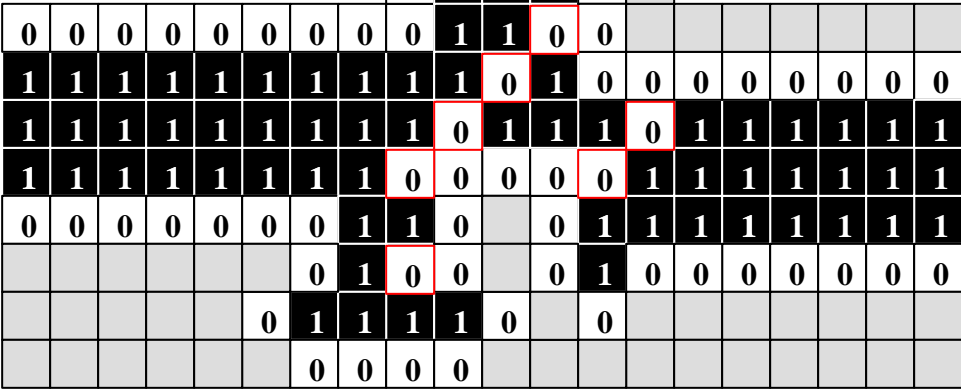


Cycle 9

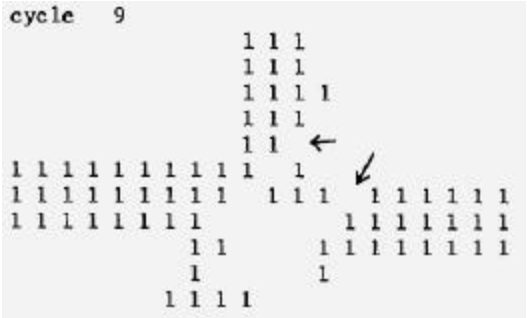
A



B

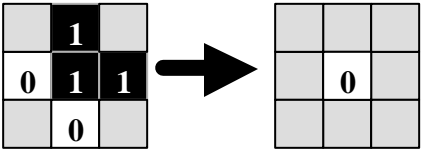


output wire

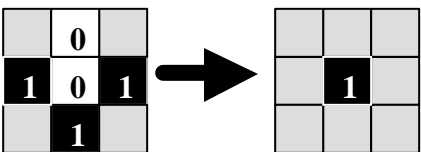


The logic function: B AND NOT A.  
 Case III, B input only. 15-cycle animation.

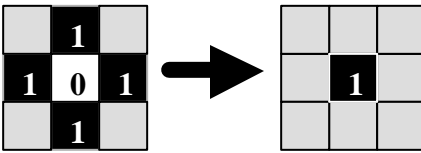
Rule 1.



Rule 2.

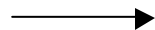
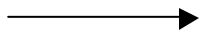
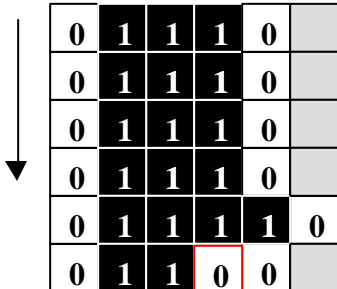


Rule 3.

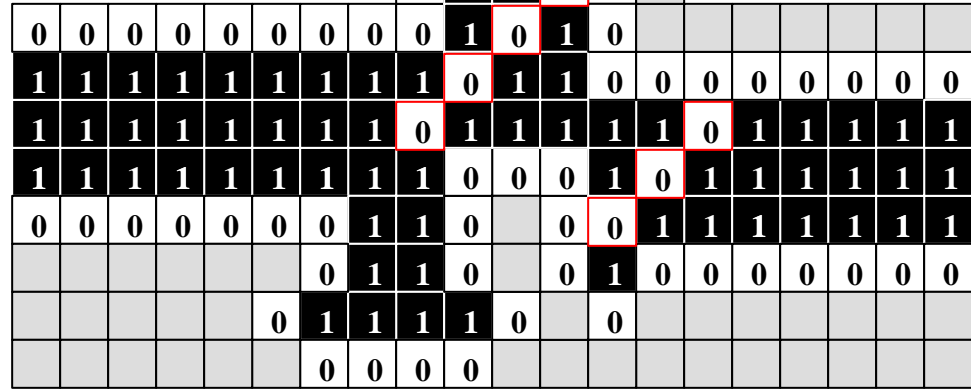


Cycle 10

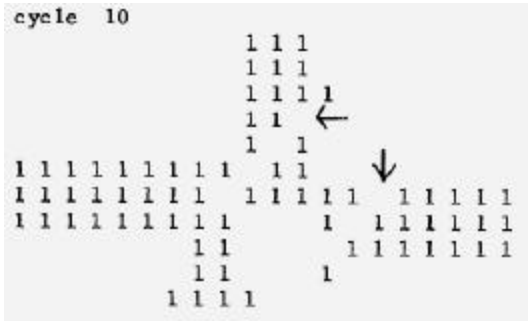
A



B

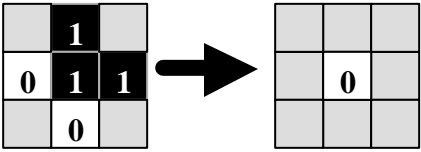


output wire

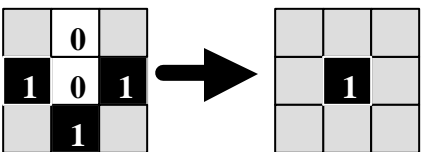


The logic function: B AND NOT A.  
 Case III, B input only. 15-cycle animation.

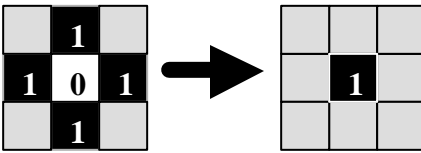
Rule 1.



Rule 2.

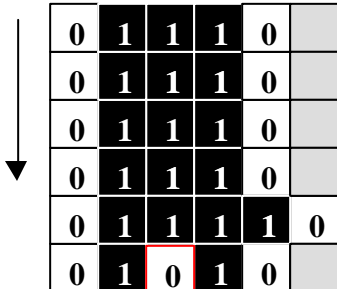


Rule 3.

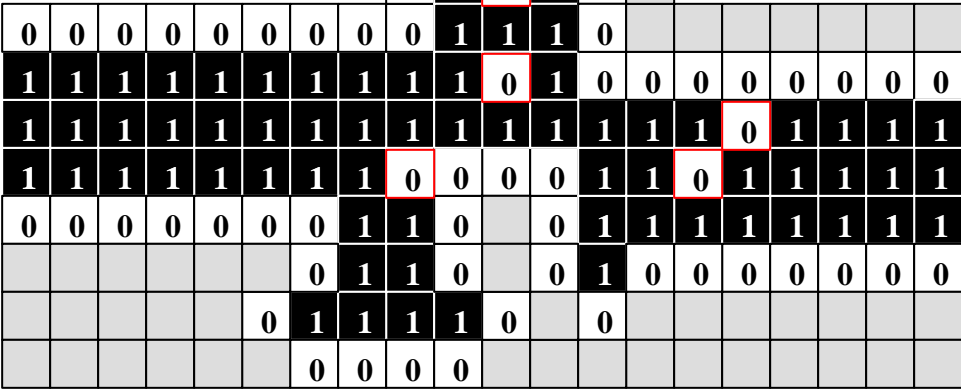


Cycle 11

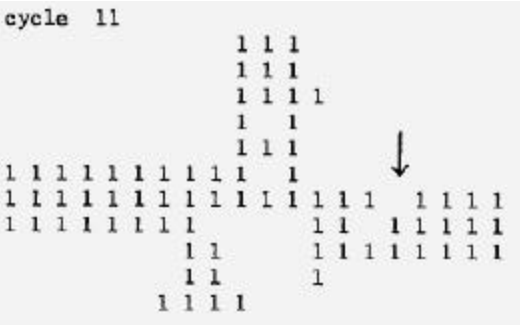
A



B

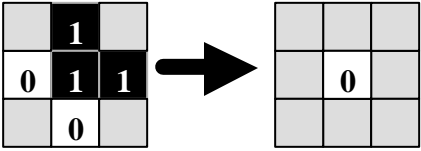


output wire

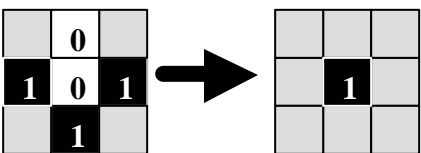


The logic function: B AND NOT A.  
 Case III, B input only. 15-cycle animation.

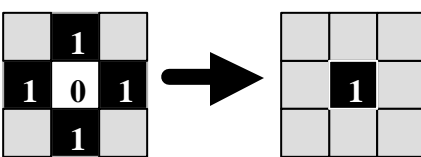
Rule 1.



Rule 2.

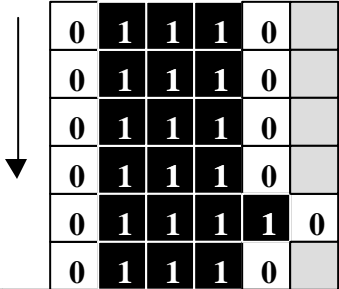


Rule 3.

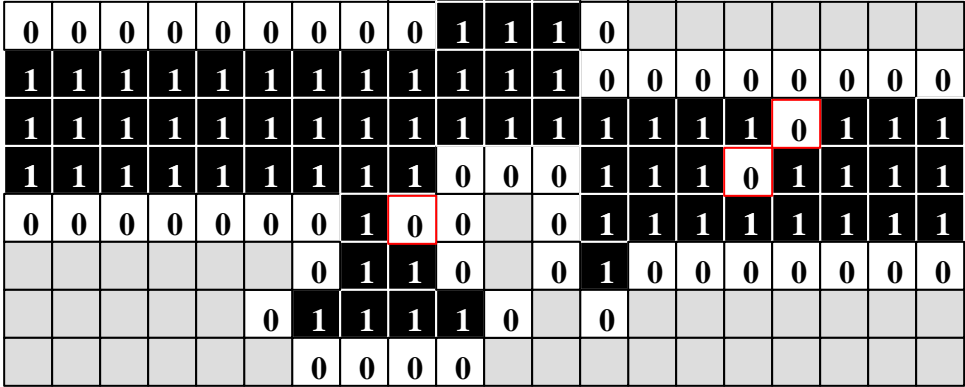


Cycle 12

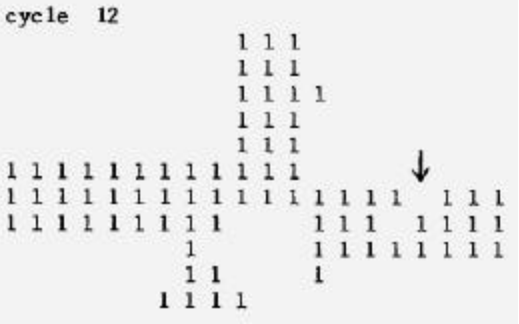
A



B

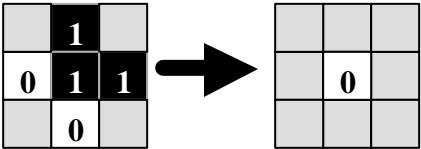


output wire

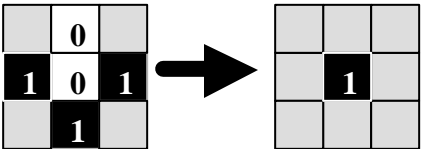


The logic function: B AND NOT A.  
 Case III, B input only. 15-cycle animation.

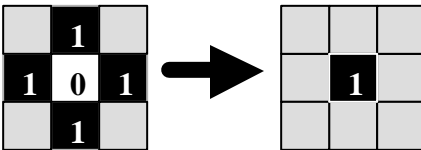
Rule 1.



Rule 2.

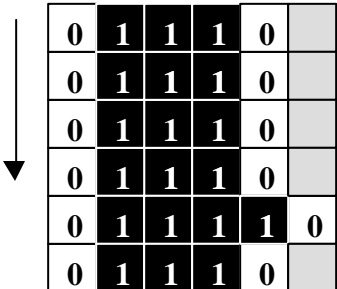


Rule 3.

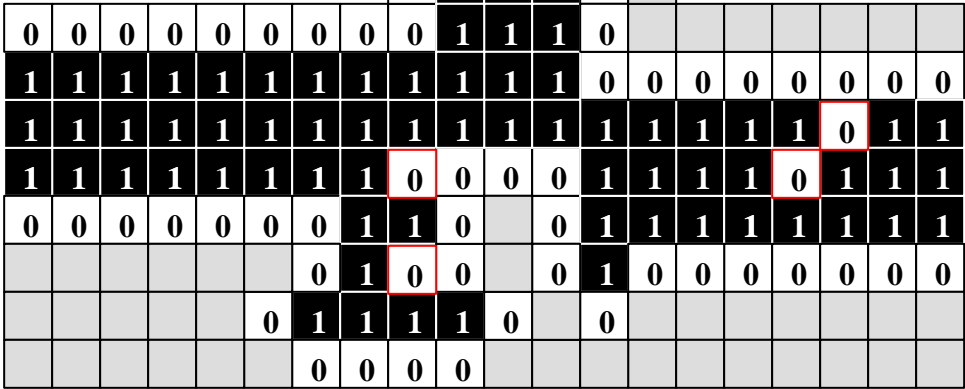


Cycle 13

A

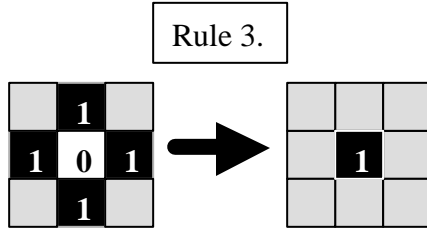
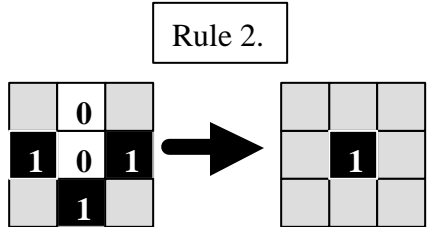
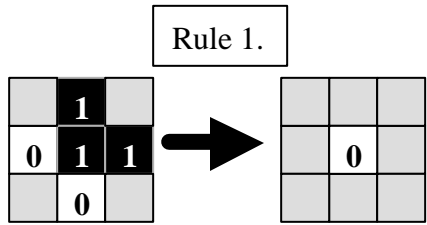


B

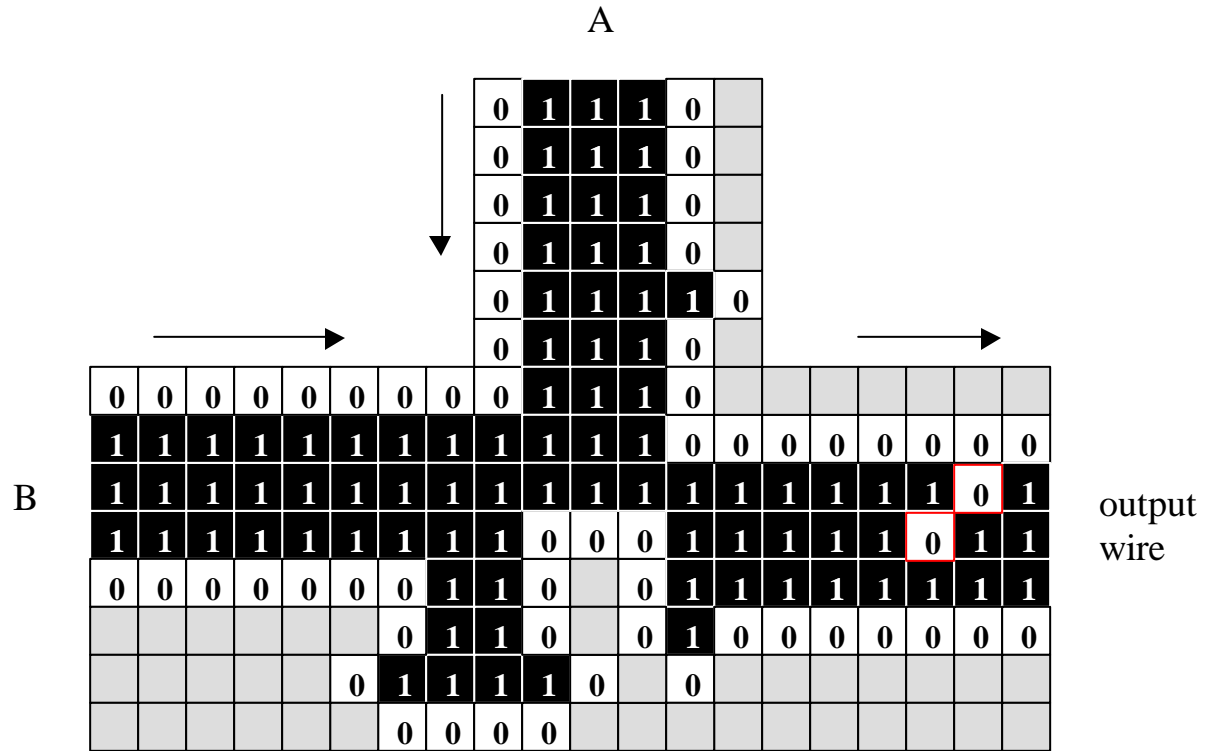


output wire

The logic function: B AND NOT A.  
 Case III, B input only. 15-cycle animation.



Cycle 14



End.